

ECE 842 Computer Architecture

Spring 2009

10:10a.m.—11:00a.m. MWF

223 Riggs Hall

Objective

This course delves into advanced research and design principles of modern computer architectures. The focus of this course is on superscalar processors. The class will include the design of a superscalar processor simulator. Students are also expected to study and present a research paper on modern processor design.

Instructor

Tarek Taha
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Office Hours

You are welcome to knock on my door anytime.

Prerequisite

ECE 629 or equivalent.

Course Text

- Modern Processor Design: Fundamentals of Superscalar Processors, by John Shen and Mikko Lipasti, McGraw-Hill, 2005.
- Research papers for reading will be handed out in class or over the web.

Course Webpage

<http://www.ces.clemson.edu/~tarek/ece842/>

Grading Policy

Your final grade for this course will be determined by the following averaging procedure (subject to change):

Midterm Exam	≈ 40 %
Final Exam	≈ 10 %
Project	≈ 30 %
Short Quizzes/Homework/Class Participation	≈ 10 %
Class Presentation	≈ 10 %

Academic Integrity

"As members of the Clemson University community, we have inherited Thomas Green Clemson's vision of this institution as a 'high seminary of learning.' Fundamental to this vision is a mutual commitment to truthfulness, honor, and responsibility, without which we cannot earn the trust and respect of others. Furthermore, we recognize that academic dishonesty detracts from the value of a Clemson degree. Therefore, we shall not tolerate lying, cheating, or stealing in any form."